FIG. 1A

<u>100</u>

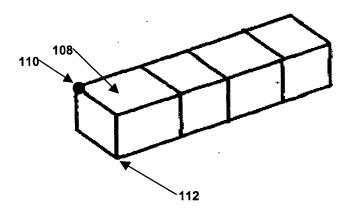


FIG. 1B 115

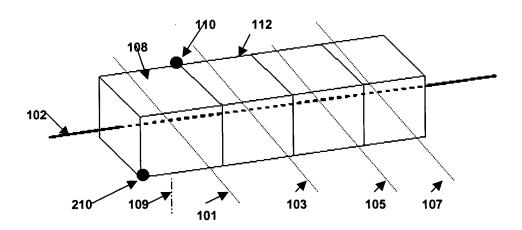


FIG. 2A

115

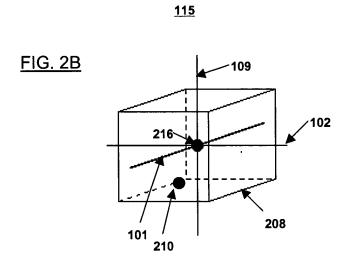
169

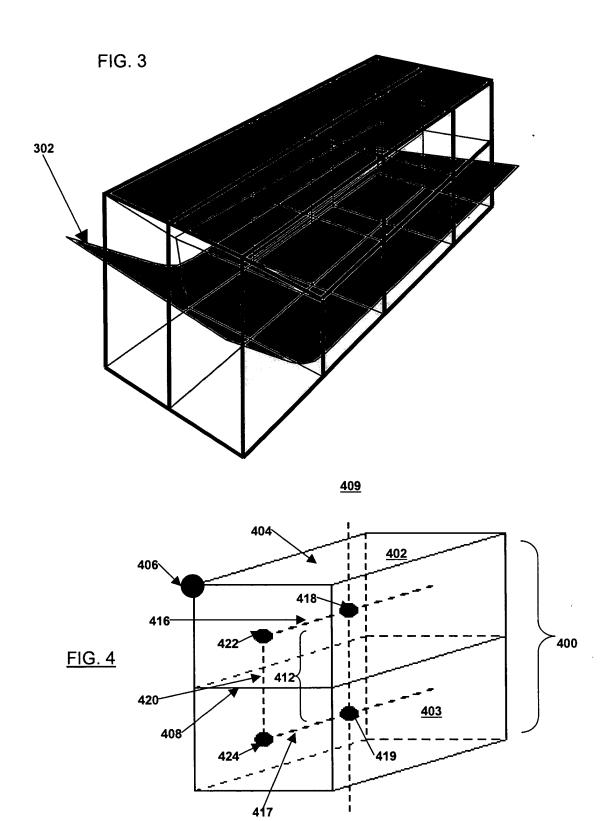
206

202

102

101





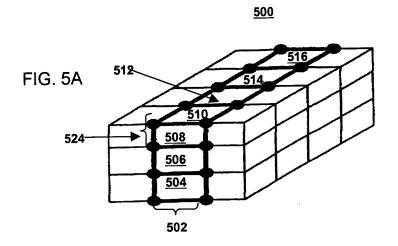
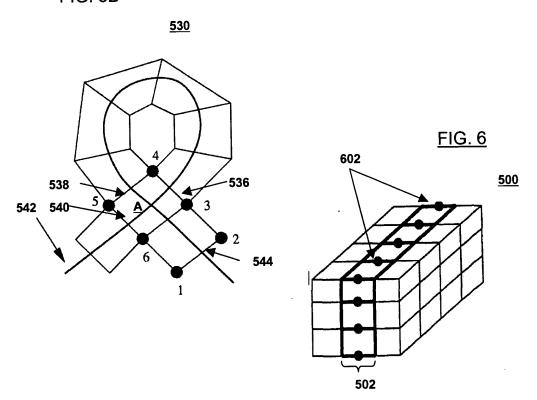


FIG. 5B



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<u>FIG. 7</u>

<u>702</u>

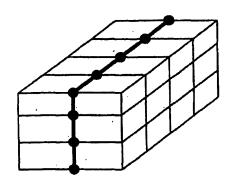
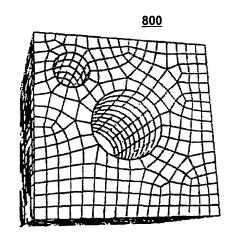


FIG. 8



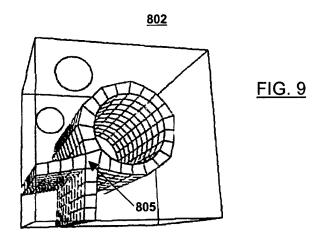


FIG. 10

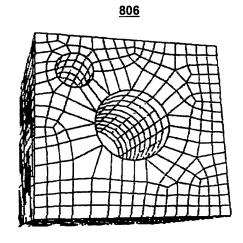


FIG. 11

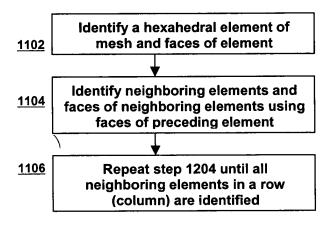
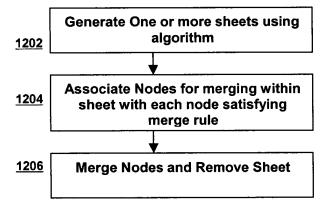
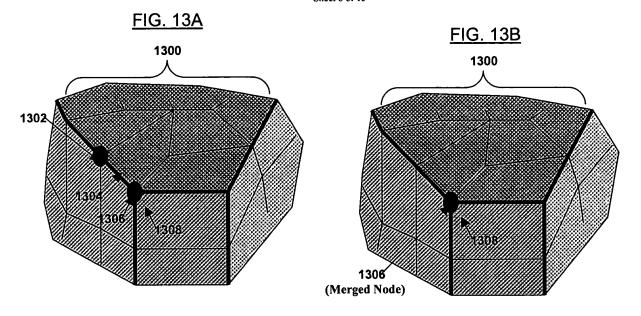
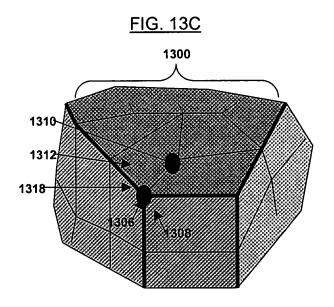


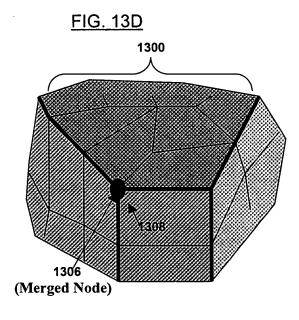
FIG. 12

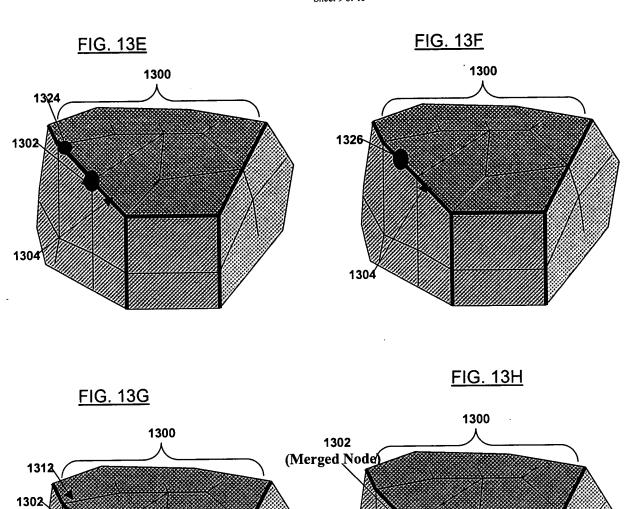


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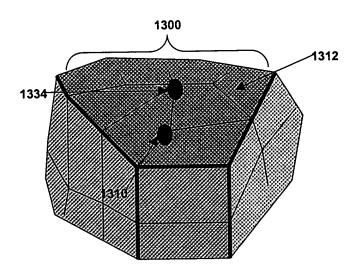






1318

FIG. 131



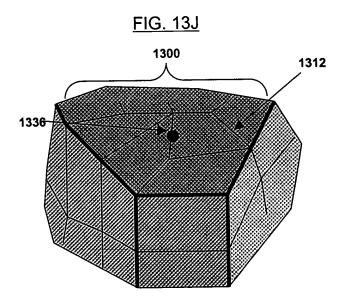


FIG. 14A

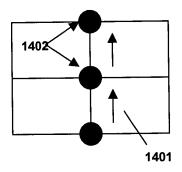


FIG. 14B

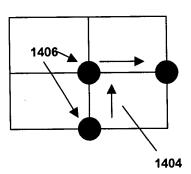
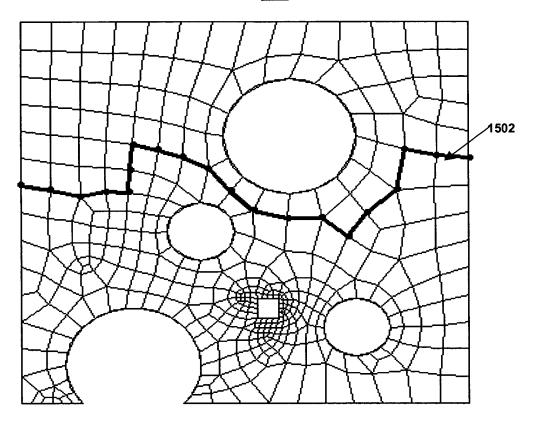


FIG. 15

<u>1500</u>



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FIG. 16A

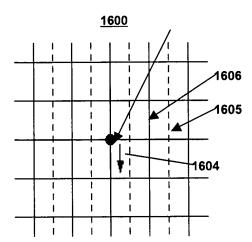


FIG. 16B

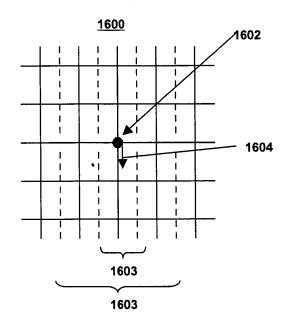


FIG. 16C

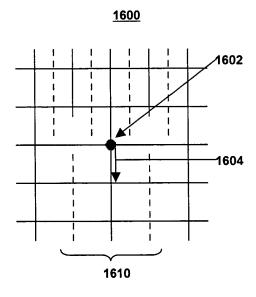


FIG. 16D

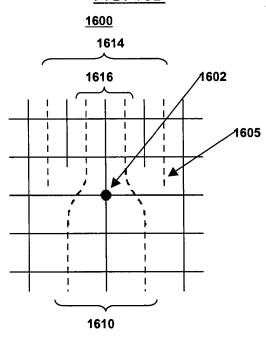


FIG. 16E

<u>1612</u>

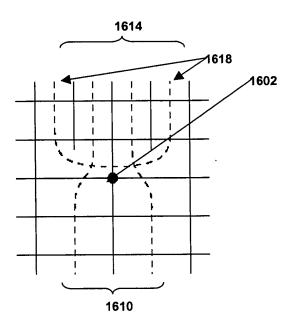


FIG. 16F

<u>1612</u>

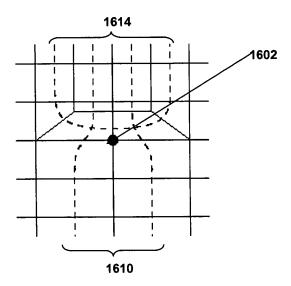
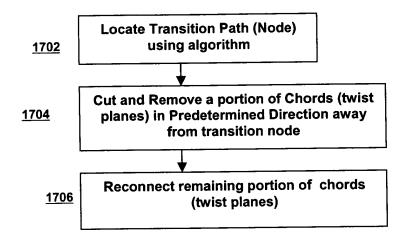


FIG. 17



Processing
Device
1804

Input Device
1802

Storage
Media
1808